



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yamazaki, et al.                      Art Unit : 2811  
Serial No. : 09/814,255                      Examiner : Ori Nadav  
Filed : March 21, 2001  
Title : SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

REPLY TO ACTION OF OCTOBER 6, 2003

The following are remarks submitted in response to the action mailed on October 6, 2003.

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